

Description

The SD4401 is a constant frequency, 6-pin SOT23 current mode step-up converter intended for small, low power applications. The SD4401 switches at 1.2MHz and allows the use of tiny, low cost capacitors and inductors 2mm or less in height. Internal soft-start results in small inrush current and extends battery life. The SD4401 features automatic shifting to pulse frequency modulation mode at light loads. The SD4401 includes under-voltage lockout, current limiting, and thermal overload protection to prevent damage in the event of an output overload. The SD4401 is available in a small 6-pin SOT-23 package.

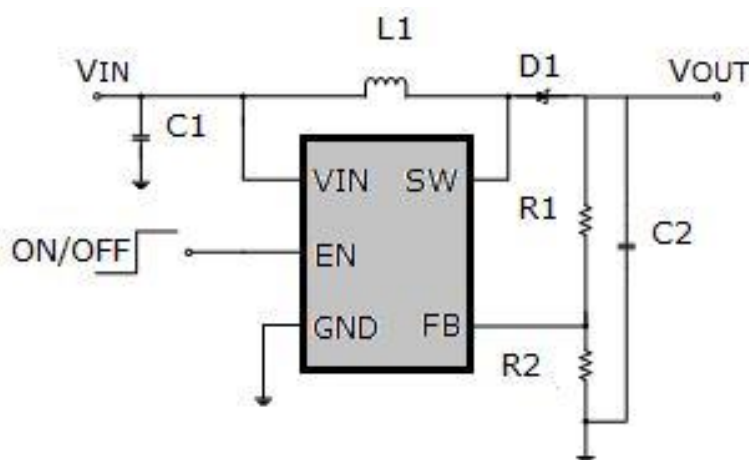
Features

- Up to 98% Integrated 80mΩ MOSFET
- 2V to 24V Input Voltage
- 1.2MHz Fixed Switching Frequency
- Internal 4A Switch Current Limit
- Adjustable Output Voltage
- Internal Compensation
- Up to 28V Output Voltage
- Automatic Pulse Frequency Modulation Mode at Light Loads
- up to 97% Efficiency
- Available in a 6-Pin SOT23-6 package

Applications

- Battery-Powered Equipment
- Set-Top Boxed
- LCD Bias Supply
- DSL and Cable Modems and Routers
- Networking cards powered from PCI or PCI express slots

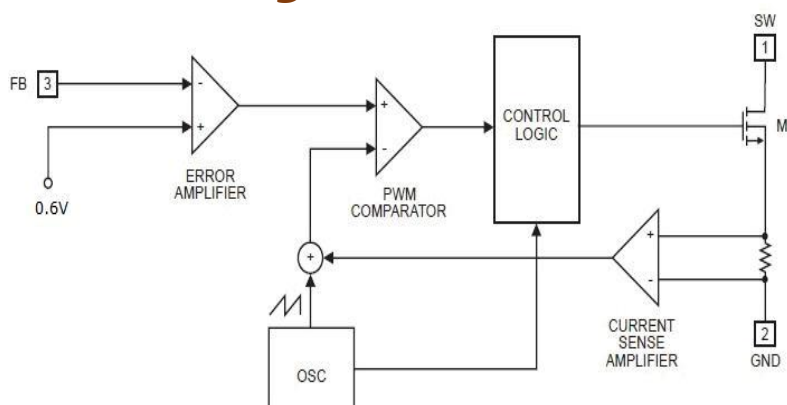
Typical Application



Pin Description

Pin Configuration	Pin	Name	Description
	1	SW	Power Switch Output. SW is the drain of the internal MOSFET switch. Connect the power inductor and output rectifier to SW. SW can swing between GND and 28V.
	2	GND	GROUND Pin
	3	FB	Feedback Input. The FB voltage is 0.6V. Connect a resistor divider to FB.
	4	EN	Regulator On/Off Control Input. A high input at EN turns on the converter, and a low input turns it off. When not used, connect EN to the input supply for automatic startup.
	5	VIN	Input Supply Pin. Must be locally bypassed.
	6	NC	NC

Functional Block Diagram



Absolute Maximum

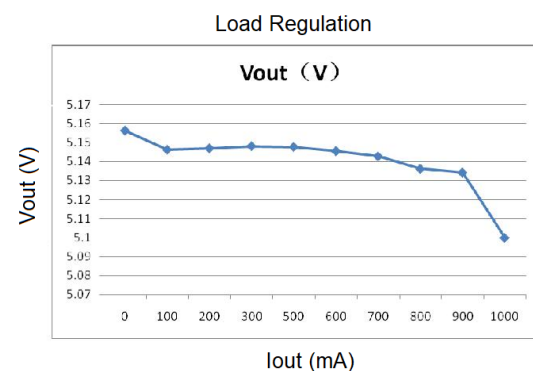
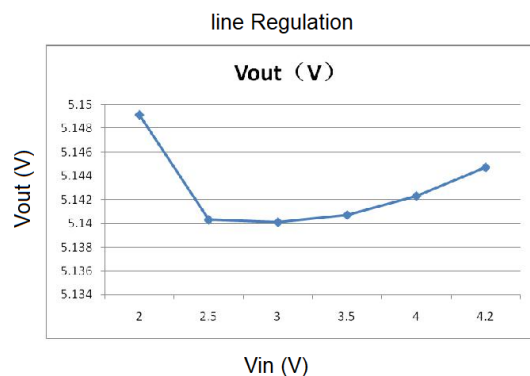
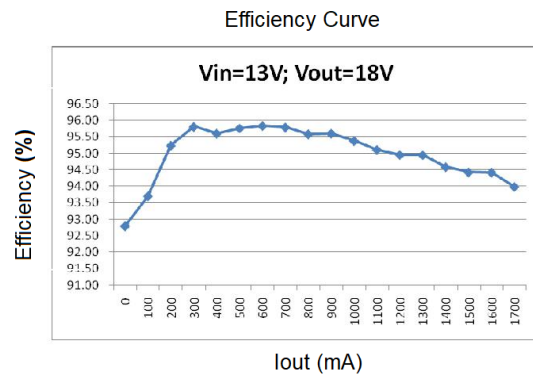
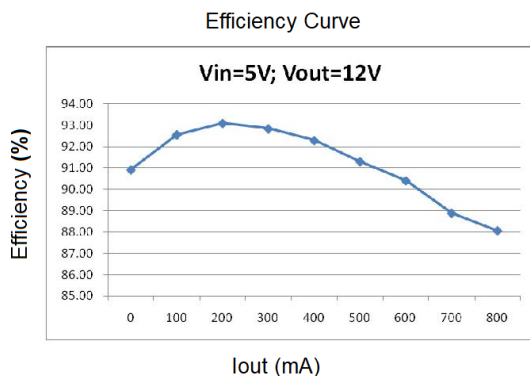
Parameter Name	Value	Unit
VIN, EN Voltage	-0.3 ~ 26	V
SW Voltage	-0.3 ~ 30	V
FB Voltages	-0.3 ~ 6	V
Maximum Junction Temperature	150	°C
Operating Temperature Range	-40 ~ +85	°C
Storage Temperature Range	-65 ~ +150	°C
Lead Temperature (Soldering, 10sec.)	260	°C

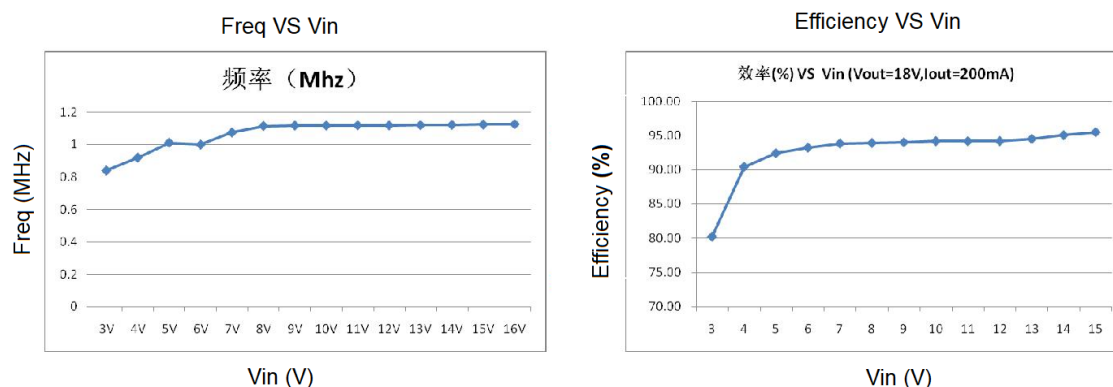
Electrical Characteristics

(VIN=VEN=5V, TA = 25°C, unless otherwise noted.)

Parameter	Conditions	Min	Typ	Max	unit
Operating Input Voltage		2		24	V
Under Voltage Lockout				1.98	V
Under Voltage Lockout Hysteresis			100		mV
Current (Shutdown)	V _{EN} = 0V		0.1	1	μA
Quiescent Current (PFM)	V _{FB} =0.7V, No switch		100	200	μA
Quiescent Current (PWM)	V _{FB} =0.5V, switch		1.6	2.2	mA
Switching Frequency			1.2		MHz
Maximum Duty Cycle	V _{FB} = 0V	90			%
EN Input High Voltage		1.5			V
EN Input Low Voltage				0.4	V
FB Voltage		0.588	0.6	0.612	V
FB Input Bias Current	V _{FB} = 0.6V	-50	-10		nA
SW On Resistance	by design, not tested		80	150	mΩ
SW Current Limit	V _{IN} = 5V, Duty cycle=50%		4		A
SW Leakage	V _{SW} = 20V			1	μA
Thermal Shutdown			155		°C

Typical Performance Characteristics





Functional Description

The SD4401 uses a fixed frequency, peak current mode boost regulator architecture to regulate voltage at the feedback pin. The operation of the SD4401 can be understood by referring to the block diagram of Figure 3. At the start of each oscillator cycle the MOSFET is turned on through the control circuitry. To prevent sub-harmonic oscillations at duty cycles greater than 50 percent, a stabilizing ramp is added to the output of the current sense amplifier and the result is fed into the negative input of the PWM comparator. When this voltage equals the output voltage of the error amplifier the power MOSFET is turned off. The voltage at the output of the error amplifier is an amplified version of the difference between the 0.6V bandgap reference voltage and the feedback voltage. In this way the peak current level keeps the output in regulation. If the feedback voltage starts to drop, the output of the error amplifier increases. These results in more current to flow through the power MOSFET, thus increasing the power delivered to the output. The SD4401 has internal soft start to limit the amount of input current at startup and to also limit the amount of overshoot on the output.

Applications Information

Setting the Output Voltage

The internal reference V_{REF} is 0.6V (Typical). The output voltage is divided by a resistor divider, R_1 and R_2 to the FB pin. The output voltage is given by:

$$V_{OUT} = V_{REF} \times \left(1 + \frac{R_1}{R_2}\right)$$

Inductor Selection

The recommended values of inductor are 4.7 to 22 μ H. Small size and better efficiency are the major concerns for portable device, such as SD4401 used for mobile phone. The inductor should have low core loss at 1.2MHz and low DCR for better efficiency. To avoid inductor saturation current rating should be considered.

Capacitor Selection

SD4401

Input and output ceramic capacitors of 22μF are recommended for SD4401 applications. For better voltage filtering, ceramic capacitors with low ESR are recommended. X5R and X7R types are suitable because of their wider voltage and temperature ranges.

Diode Selection

Schottky diode is a good choice for SD4401 because of its low forward voltage drop and fast reverse recovery. Using Schottky diode can get better efficiency. The high speed rectification is also a good characteristic of Schottky diode for high switching frequency. Current rating of the diode must meet the root mean square of the peak current and output average current multiplication as following:

$$I_D(RMS) \approx \sqrt{I_{OUT} \times I_{PEAK}}$$

The diode's reverse breakdown voltage should be larger than the output voltage.

PCB Layout Guide

When laying out the printed circuit board, the following checking should be used to ensure proper operation of the SD4401.

Check the following in your layout:

- The power traces, consisting of the GND trace, the SW trace and the VIN trace should be kept short, direct and wide.
- Does the (+) plates of Cin connect to Vin as closely as possible. This capacitor provides the AC current to the internal power MOSFETs.
- Keep the switching node SW away from the sensitive VOUT node.
- Keep the (-) plates of Cin and Cout as close as possible.

Package outline

